

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Yasushi AMAMIYA  
 Title: LOGIC CIRCUIT WITH RESTRAINED LEAK CURRENT TO  
 DIFFERENTIAL CIRCUIT  
 Appl. No.: Unassigned  
 Filing Date: 12/15/2005  
 Examiner: Unassigned  
 Art Unit: Unassigned

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR §1.56**

Mail Stop Amendment  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Sir:

Submitted herewith on Form PTO/SB/08 is a listing of documents known to Applicant in order to comply with Applicant's duty of disclosure pursuant to 37 CFR §1.56.

A copy of each non-U.S. patent document and each non-patent document is being submitted to comply with the provisions of 37 CFR §1.97 and §1.98.

The submission of any document herewith, which is not a statutory bar, is not intended as an admission that such document constitutes prior art against the claims of the present application or that such document is considered material to patentability as defined in 37 CFR §1.56(b). Applicant does not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a *prima facie* art reference against the claims of the present application.

**TIMING OF THE DISCLOSURE**

The listed documents are being submitted in compliance with 37 CFR §1.97(b), within three (3) months of the date of entry of the national stage as set forth in 37 CFR §1.491.

**RELEVANCE OF EACH DOCUMENT**

Document A1 is a U.S. counterpart of Document A6.

Document A2 is a U.S. counterpart of Document A9.

Document A3 is a U.S. counterpart of Document A10.

Documents A4, A5 and A13 listed on the attached PTO/SB/08 were cited as being relevant during the prosecution of the corresponding International application. A copy of the International Search Report is attached setting forth the portion of each document considered relevant by the examiner. An English translation of each of the foreign-language document is not readily available. However, the absence of such translation does not relieve the PTO from its duty to consider the submitted foreign language document (37 CFR §1.98 and MPEP §609). English language abstracts are attached.

Document A6 relates to an ECL D latch circuit and ECL D flip-flop utilizing the same.

Document A7 relates to a bipolar logic circuit.

Document A8 relates to an ECL circuit.

Document A9 relates to an bipolar latch circuit provided with clock signal level conversion function, and flip-flop circuit.

The relevance of Documents A10 and A11 is described in the present application.

Document A12 relates to a flip-flop circuit used to lower a power source voltage.

Applicant respectfully requests that each listed document be considered by the Examiner and be made of record in the present application and that an initialed copy of Form PTO/SB/08 be returned in accordance with MPEP §609.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 CFR §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741.

Respectfully submitted,

Date: December 15, 2005

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MODIFIED BY USPTO (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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APPROVED FOR PTO 15 DEC 2005

Substitute for form 1449B/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  Date Submitted: December 15, 2005  (use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	Unassigned
Sheet 1 of 1				Filing Date	12/15/2005
				First Named Inventor	Yasushi AMAMIYA
				Group Art Unit	Unassigned
				Examiner Name	Unassigned
				Attorney Docket Number	040373-0367

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
	A1	5,900,760		LEE	05-04-1999	
	A2	5,289,055		RAZAVI	02-22-1994	
	A3	4,977,335		OGAWA	12-11-1990	

U.S. PATENT APPLICATION DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Application Document		Name of Patentee or Applicant of Cited Document	Filing Date of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Serial Number	Kind Code <sup>2</sup> (if known)			

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Documents	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
	A4	JP	2002-290212		NEC CORP.	10-04-2002		A
	A5	JP	11-103240		TOSHIBA CORP.	04-13-1999		A
	A6	JP	10-075162		SAMSUNG ELECTRONICS CO. LTD.	03-17-1998		A
	A7	JP	08-018435		NIPPON TELEGR & TELEP CORP.	01-19-1996		A
	A8	JP	07-030405		NIPPON TELEGR & TELEP CORP.	01-31-1995		A
	A9	JP	06-224738		AT&T	08-12-1994		A
	A10	JP	02-021717		TOSHIBA CORP.	01-24-1990		A
	A11	JP	63-086611		NEC CORP.	04-18-1988		A
	A12	JP	63-220615		NEC CORP.	09-13-1988		A
	A13	JP	62-224109		MATSUSHITA ELECTRIC IND. CO. LTD.	10-02-1987		A

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>6</sup>	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

<sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.